



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Dileep Sivasankaran, et al.

Examiner: not yet known

Serial No.: 10/678,977

Art Unit: 2661

Filed: October 3, 2003

Atty. Docket No. 10030.000210 (GCI-002)

For: LAN Switch with Rapid Fault Recovery

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR STATUS OF APPLICATION

Sir:

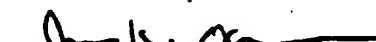
Applicants request a status of the above-identified patent application. Applicants have not received a communication from the U.S. Patent and Trademark Office since receiving the Filing Receipt of Application mailed on December 31, 2003.

Please inform the undersigned, at the below stated address, of the status of this application.

Favorable action is respectfully solicited.

Respectfully submitted,
Dileep Sivasankaran, et al.

Dated: September 20, 2005

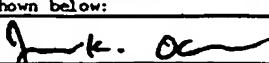
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Enclosure(s)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature: 			
Typed or Printed Name: James K. Okamoto	Dated:	September 20, 2005	